

Features

- Ballast control and half bridge driver in one IC
- Programmable preheat frequency
- Programmable preheat time
- Internal ignition ramp
- Programmable over-current threshold
- Programmable run frequency
- Programmable dead time
- DC bus under-voltage reset
- Shutdown pin with hysteresis
- Internal 15.6V zener clamp diode on Vcc
- Micropower startup (150 μ A)
- Latch immunity and ESD protection

Description

The IR2156 incorporates a high voltage half-bridge gate driver with a programmable oscillator and state diagram to form a complete ballast control IC. The IR2156 features include programmable preheat and run frequencies, programmable preheat time, programmable dead-time, and programmable over-current protection. Comprehensive protection features such as protection from failure of a lamp to strike, filament failures, as well as an automatic restart function, have been included in the design.

Packages



IR2156SPBF
SOICN14



IR2156PBF
PDIP14

Application Diagram

