

**PS21964-4S****INTEGRATED POWER FUNCTIONS**

600V/15A low-loss 5<sup>th</sup> generation IGBT inverter bridge for three phase DC-to-AC power conversion.  
Open emitter type.

**INTEGRATED DRIVE, PROTECTION AND SYSTEM CONTROL FUNCTIONS**

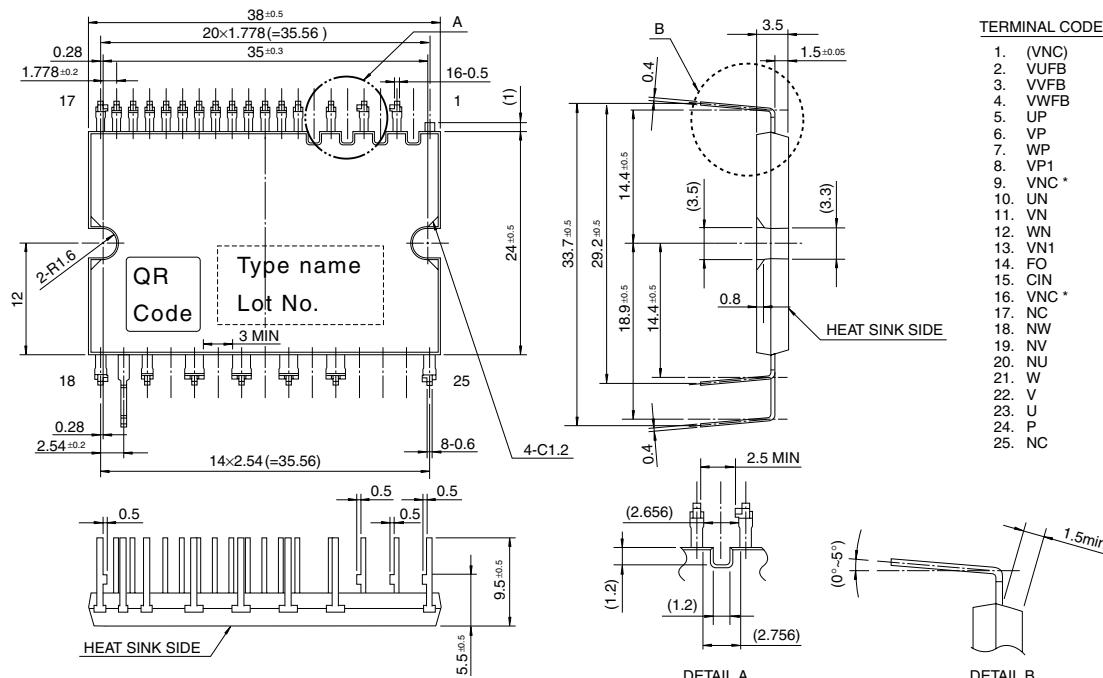
- For upper-leg IGBTs : Drive circuit, High voltage high-speed level shifting, Control supply under-voltage (UV) protection.
- For lower-leg IGBTs : Drive circuit, Control supply under-voltage protection (UV), Short circuit protection (SC).
- Fault signaling : Corresponding to an SC fault (Lower-leg IGBT) or a UV fault (Lower-side supply).
- Input interface : 3V, 5V line (High Active).
- UL Approved : Yellow Card No. E80276

**APPLICATION**

AC100V~200V inverter drive for small power motor control.

**Fig. 1 PACKAGE OUTLINES**

Dimensions in mm



\*) Two VNC terminals (9 & 16 pin) are connected inside DIP-IPM, please connect either one to the 15V power supply GND outside and leave another one open.

**MAXIMUM RATINGS** ( $T_j = 25^\circ\text{C}$ , unless otherwise noted)**INVERTER PART**

Symbol	Parameter	Condition	Ratings	Unit
Vcc	Supply voltage	Applied between P-NU, NV, NW	450	V
VCC(surge)	Supply voltage (surge)	Applied between P-NU, NV, NW	500	V
Vces	Collector-emitter voltage		600	V
$\pm I_C$	Each IGBT collector current	$T_c = 25^\circ\text{C}$	15	A
$\pm I_{CP}$	Each IGBT collector current (peak)	$T_c = 25^\circ\text{C}$ , less than 1ms	30	A
Pc	Collector dissipation	$T_c = 25^\circ\text{C}$ , per 1 chip	33.3	W
Tj	Junction temperature	(Note 1)	-20~+125	$^\circ\text{C}$

**Note 1:** The maximum junction temperature rating of the power chips integrated within the DIP-IPM is  $150^\circ\text{C}$  (@  $T_c \leq 100^\circ\text{C}$ ). However, to ensure safe operation of the DIP-IPM, the average junction temperature should be limited to  $T_{j(\text{ave})} \leq 125^\circ\text{C}$  (@  $T_c \leq 100^\circ\text{C}$ ).

**CONTROL (PROTECTION) PART**

Symbol	Parameter	Condition	Ratings	Unit
VD	Control supply voltage	Applied between VP1-VNC, VN1-VNC	20	V
VDB	Control supply voltage	Applied between VUFB-U, VVFB-V, VWFB-W	20	V
VIN	Input voltage	Applied between UP, VP, WP, UN, VN, WN-VNC	-0.5~VD+0.5	V
VFO	Fault output supply voltage	Applied between FO-VNC	-0.5~VD+0.5	V
IFO	Fault output current	Sink current at Fo terminal	1	mA
Vsc	Current sensing input voltage	Applied between CIN-VNC	-0.5~VD+0.5	V

**TOTAL SYSTEM**

Symbol	Parameter	Condition	Ratings	Unit
Vcc(prot)	Self protection supply voltage limit (short circuit protection capability)	$VD = 13.5\sim16.5\text{V}$ , Inverter part $T_j = 125^\circ\text{C}$ , non-repetitive, less than $2\mu\text{s}$	400	V
Tc	Module case operation temperature	(Note 2)	-20~+100	$^\circ\text{C}$
Tstg	Storage temperature		-40~+125	$^\circ\text{C}$
Viso	Isolation voltage	60Hz, Sinusoidal, 1 minute, Between pins and heat-sink plate	1500	Vrms

**Note 2:** Tc measurement point

