

SONY CXA1464AS/CXA1465AS

Color TV Y/C/Jungle/RGB Interface/Deflection Compensation

Description

CXA1464AS/CXA1465AS is bipolar ICs, each having the NTSC color TV luminance signal processing, chrominance signal processing, sync signal processing, RGB interface, auto cut-off, and deflection compensation functions integrated into a chip.

Features

- Compatible with I²C bus. Two bus lines SDA and SCL drive 23 self-contained D/A converters to provide various types of adjustments and user controls.
- Has a Deflection compensation circuit which facilitates adjustments for compensation of deflection distortion.
- Has an auto cut-off function for automatic CRT cut-off adjustment and compensation for changes with time.
- Has built-in dynamic picture capability.
- Has multiple inputs (Y/C separate inputs, analog R, G and B inputs, digital R, G and B inputs for screen display).
- Wide band R, G and B interface (typ. -3dB at 20MHz)

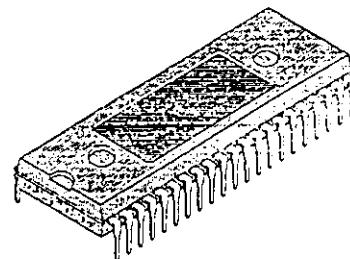
Absolute Maximum Ratings (Ta=25°C)

• Supply voltage	V_{CC}	12	V
• Operating temperature	T_{opr}	-20 to +75	°C
• Storage temperature	T_{stg}	-65 to +150	°C
• Allowable power dissipation	P_D	1.9	W

Operating Condition

• Supply voltage	V_{CC}	8.5 to 9.5	V
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48pin SDIP (Plastic)



Applications

TV sets

Note that the CXA1464AS and CXA1465AS are different in demodulation axis.

CXA1464AS: Compatible with Japanese standard

CXA1465AS: Compatible with U.S.A. standard

Structure

Bipolar silicon monolithic IC

Pin Configuration

