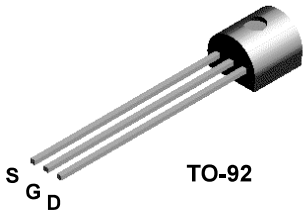
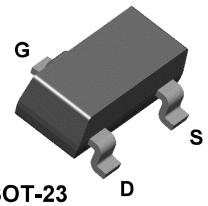
FAIRCHILD
SEMICONDUCTOR TM

J174
J175
J176
J177

MMBFJ175
MMBFJ176
MMBFJ177



TO-92



SOT-23
Mark: 6W / 6X / 6Y

NOTE: Source & Drain are interchangeable

P-Channel Switch

This device is designed for low level analog switching sample and hold circuits and chopper stabilized amplifiers. Sourced from Process 88.

Absolute Maximum Ratings* TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{DG}	Drain-Gate Voltage	- 30	V
V_{GS}	Gate-Source Voltage	30	V
I_{GF}	Forward Gate Current	50	mA
T_J , T_{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:
1) These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	Characteristic	Max		Units
		J174 -177	*MMBFJ175-177	
P_D	Total Device Dissipation Derate above 25°C	350 2.8	225 1.8	mW mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	556	°C/W

*Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."