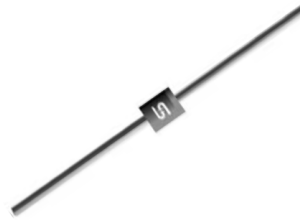


## Glass Passivated Super Fast Rectifiers

**FEATURES**

- Glass passivated chip junction
- High current capability, Low VF
- High reliability
- High surge current capability
- Low power loss
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition


**DO-201AD**

**MECHANICAL DATA**
**Case:** DO-201AD

Molding compound, UL flammability classification rating 94V-0

Base P/N with suffix "G" on packing code - green compound (halogen-free)

Base P/N with prefix "H" on packing code - AEC-Q101 qualified

**Terminal:** Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 1A whisker test

with prefix "H" on packing code meet JESD 201 class 2 whisker test

**Weight:** 1.1 g (approximately)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T <sub>A</sub> =25°C unless otherwise noted)												
PARAMETER	SYMBOL	SF 31G	SF 32G	SF 33G	SF 34G	SF 35G	SF 36G	SF 37G	SF 38G	UNIT		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	50	100	150	200	300	400	500	600	V		
Maximum RMS voltage	V <sub>RMS</sub>	35	70	105	140	210	280	350	420	V		
Maximum DC blocking voltage	V <sub>DC</sub>	50	100	150	200	300	400	500	600	V		
Maximum average forward rectified current	I <sub>F(AV)</sub>	3								A		
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	125								A		
Maximum instantaneous forward voltage (Note 1) @ 3 A	V <sub>F</sub>	0.95			1.3		1.7			V		
Maximum reverse current @ rated VR T <sub>J</sub> =25 °C T <sub>J</sub> =125 °C	I <sub>R</sub>	5				100				μA		
Maximum reverse recovery time (Note 2)	T <sub>rr</sub>	35								ns		
Typical junction capacitance (Note 3)	C <sub>j</sub>	80				60				pF		
Typical thermal resistance	R <sub>θJC</sub> R <sub>θJL</sub> R <sub>θJA</sub>	9				10				35		°C/W
Operating junction temperature range	T <sub>J</sub>	- 55 to +150								°C		
Storage temperature range	T <sub>STG</sub>	- 55 to +150								°C		

Note 1: Pulse Test with PW=300μs, 1% Duty Cycle

 Note 2: Reverse Recovery Test Conditions: I<sub>F</sub>=0.5A, I<sub>R</sub>=1.0A, I<sub>RR</sub>=0.25A

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.