

OptiMOS® Power-Transistor
Features

- N-channel
- Enhancement mode
- Logic level
- Excellent gate charge x $R_{DS(on)}$ product (FOM)
- Superior thermal resistance
- 175 °C operating temperature
- Avalanche rated
- dv/dt rated

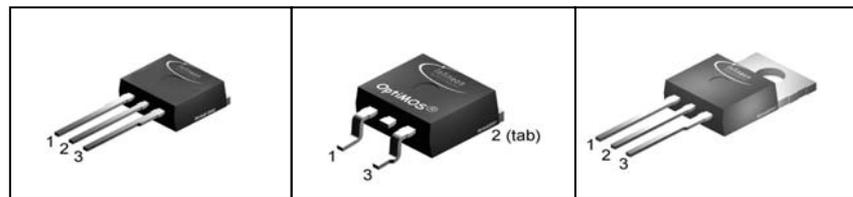
Product Summary

| | | |
|------------------|------|----|
| V_{DS} | 30 | V |
| $R_{DS(on),max}$ | 12.9 | mΩ |
| I_D | 42 | A |

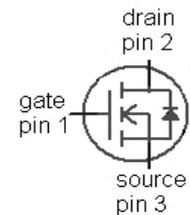
P-TO262-3-1

P-TO263-3-2

P-TO220-3-1



| Type | Package | Ordering Code | Marking |
|----------------|-------------|---------------|---------|
| SPP42N03S2L-13 | P-TO220-3-1 | Q67042-S4034 | 2N03L13 |
| SPB42N03S2L-13 | P-TO263-3-2 | Q67042-S4035 | 2N03L13 |
| SPI42N03S2L-13 | P-TO262-3-1 | Q67042-S4104 | 2N03L13 |


Maximum ratings, at $T_j=25\text{ °C}$, unless otherwise specified

| Parameter | Symbol | Conditions | Value | Unit |
|--|-------------------|--|-------------|-------|
| Continuous drain current ¹⁾ | I_D | $T_C=25\text{ °C}$ | 42 | A |
| | | $T_C=100\text{ °C}$ | 42 | |
| Pulsed drain current | $I_{D,pulse}$ | $T_C=25\text{ °C}$ | 248 | |
| Avalanche energy, single pulse | E_{AS} | $I_D=42\text{ A}$, $R_{GS}=25\text{ Ω}$ | 110 | mJ |
| Repetitive avalanche energy | E_{AR} | limited by T_{jmax} ²⁾ | 8 | mJ |
| Reverse diode dv/dt | dv/dt | $I_D=42\text{ A}$, $V_{DS}=24\text{ V}$, $di/dt=200\text{ A/μs}$, $T_{j,max}=175\text{ °C}$ | 6 | kV/μs |
| Gate source voltage | V_{GS} | | ±20 | V |
| Power dissipation | P_{tot} | $T_C=25\text{ °C}$ | 83 | W |
| Operating and storage temperature | T_j , T_{stg} | | -55 ... 175 | °C |
| IEC climatic category; DIN IEC 68-1 | | | 55/175/56 | |