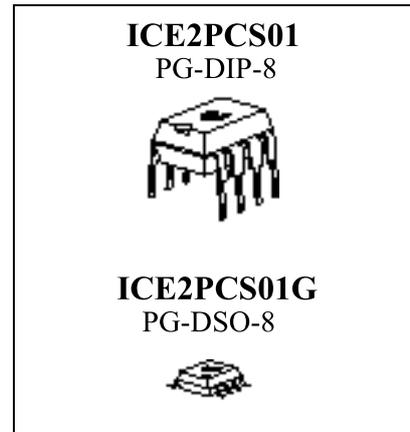


Standalone Power Factor Correction (PFC) Controller in Continuous Conduction Mode (CCM) Product Highlights

- Leadfree DIP and DSO Package
- Wide Input Range
- Optimized for applications which require fast Startup
- Output Power Controllable by External Sense Resistor
- Programmable Operating Frequency
- Output Over-Voltage Protection
- Fast Output Dynamic Response during Load Jumps



Features

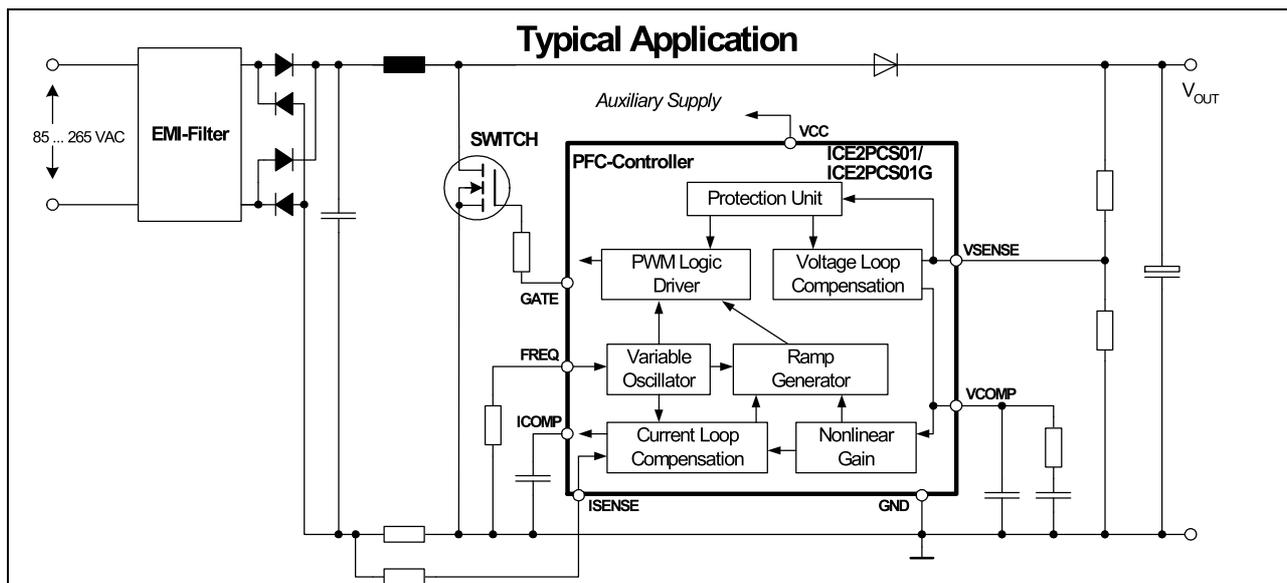
- Ease of Use with Few External Components
- Supports Wide Range
- Average Current Control
- External Current and Voltage Loop Compensation for Greater User Flexibility
- Programmable Operating/Switching Frequency (50kHz - 250kHz)
- Max Duty Cycle of 95% (at 25°C) at 125kHz
- Trimmed Internal Reference Voltage (3V±2% at 25°C)
- VCC Under-Voltage Lockout
- Cycle by Cycle Peak Current Limiting
- Output Over-Voltage Protection
- Open Loop Detection
- Enhanced Dynamic Response
- Short Startup(SoftStart) duration
- Fulfills Class D Requirements of IEC 1000-3-2
- Soft Overcurrent Protection

Description

The ICE2PCS01/G is a 8-pin wide input range controller IC for active power factor correction converters. It is designed for converters in boost topology, and requires few external components. Its power supply is recommended to be provided by an external auxiliary supply which will switch on and off the IC.

The IC operates in the CCM with average current control, and in DCM only under light load condition. The switching frequency is programmable by the resistor at pin 4. Both compensations for the current and voltage loop are external to allow full user control.

There are various protection features incorporated to ensure safe system operation conditions. The internal reference is trimmed (3V±2%) to ensure precise protection and control level. The device has a fast startup time with controlled peak start up current.



Type	Package
ICE2PCS01	PG-DIP-8
ICE2PCS01G	PG-DSO-8

1 Pin Configuration and Functionality

1.1 Pin Configuration

Pin	Symbol	Function
1	GND	IC Ground
2	ICOMP	Current Loop Compensation
3	ISENSE	Current Sense Input
4	FREQ	Switching Frequency Setting
5	VCOMP	Voltage Loop Compensation
6	VSENSE	V _{OUT} Sense (Feedback) Input
7	VCC	IC Supply Voltage
8	GATE	Gate Drive Output

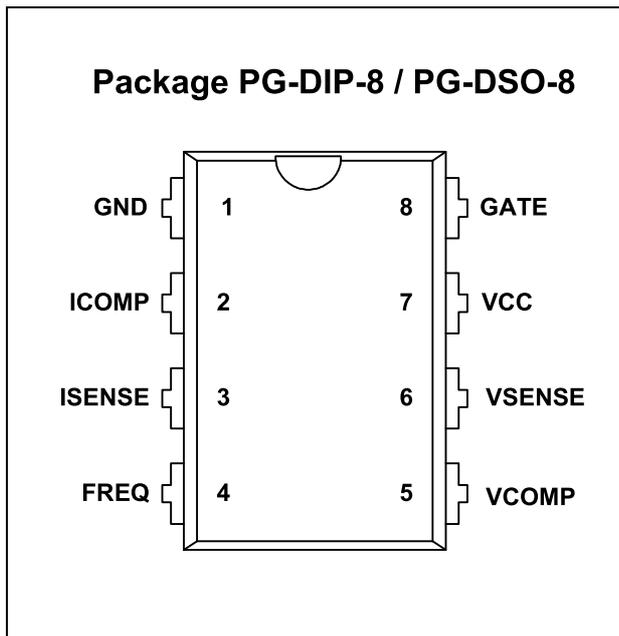


Figure 1 Pin Configuration (top view)

1.2 Pin Functionality

GND (Ground)

The ground potential of the IC.

ICOMP (Current Loop Compensation)

Low pass filter and compensation of the current control loop. The capacitor which is connected at this pin integrates the output current of OTA2 and averages the current sense signal.

ISENSE (Current Sense Input)

The ISENSE Pin senses the voltage drop at the external sense resistor (R1). This is the input signal for the average current regulation in the current loop. It is also fed to the peak current limitation block.

During power up time, high inrush currents cause high negative voltage drop at R1, driving currents out of pin 3 which could be beyond the absolute maximum ratings. Therefore a series resistor (R2) of around 220Ω is recommended in order to limit this current into the IC.

FREQ (Frequency Setting)

This pin allows the setting of the operating switching frequency by connecting a resistor to ground. The frequency range is from 50kHz to 250kHz.

VSENSE (Voltage Sense/Feedback)

The output bus voltage is sensed at this pin via a resistive divider. The reference voltage for this pin is 3V.

VCOMP (Voltage Loop Compensation)

This pin provides the compensation of the output voltage loop with a compensation network to ground (see Figure 2). This also gives the soft start function which controls an increasing AC input current during start-up.

VCC (Power Supply)

The VCC pin is the positive supply of the IC and should be connected to an external auxiliary supply. The operating range is between 11V and 26V. The turn-on threshold is at 11.8V and under voltage occurs at 11V. There is no internal clamp for a limitation of the power supply.

GATE

The GATE pin is the output of the internal driver stage, which has a capability of 1.5A instantaneous source and 2.0A instantaneous sink current.

Its gate drive voltage is clamped at 15V (typically).

2 Representative Block diagram

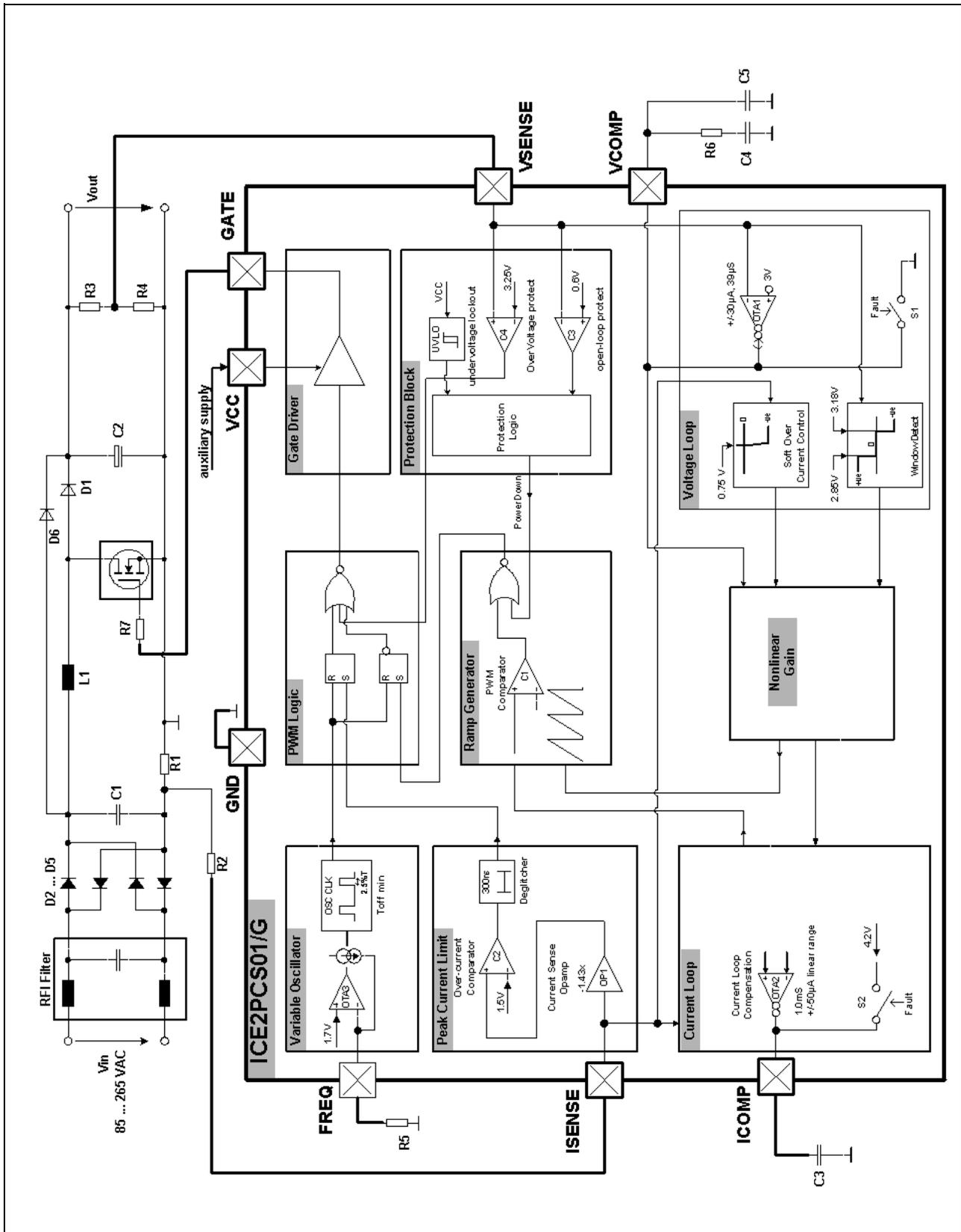


Figure 2 Representative Block diagram