

STRUCTURE Silicon Monolithic Integrated circuit
TYPE 1.0A Low-Dropout Voltage Regulator with Shut down Switch
PRODUCT SERIES **B A X X B C O W F P**
FEATURES

- Maximum Output Current : 1A
- High Precision Output Voltage : $\pm 2\%$

○ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply Voltage ※1	Vcc	-0.3~18.0	V
Control Input Voltage	VCTL	-0.3~Vcc	V
Power dissipation ※2	Pd	1300	mW
Operating temperature range	Topr	-40~105	°C
Power dissipation	Tstg	-55~+150	°C
Junction temperature	Tjmax	150	°C

※1 Do not however exceed Pd.

※2 Derating in done at 10.4mW/°C for operating above Ta=25°C.

○OPERATING CONDITIONS (Ta=-40~105°C, Do not however exceed Pd.)

Parameter	Symbol	Min	Max	Unit
Input Voltage	Vcc	3.0	16.0	V
Output current	Io	-	1.0	A
Control Pin Input Voltage	Vctl	0	Vcc	V

※3 When Output voltage is 1.5V, 1.8V, 2.5V, Vccmin=3.0V
 When Output voltage is over 3.0V, Vccmin=Vo+1.0V

NOTE : The product described in this specification is a strategic product (and/or service) subject to COCOM regulations. It should not be exported without authorization from the appropriate government.

NOTE : This product is not designed for protection against radioactive rays.

Status of this document

The Japanese version of this document is the formal specification.

A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document, formal version takes priority.

○ELECTRICAL CHARACTERISTICS

Unless otherwise specified, Ta=25°C, Vcc=3.3V(Vo=1.5V,1.8V,2.5V), Vcc=5.0V(Vo=3.0V,3.3V), Vcc=Vo+3.0V(Vo≥5.0V)

Parameter	Symbol	Limit			Unit	Conditions
		Min.	Typ.	Max		
Shut Down Current	I _{sd}	—	0	10	μA	V _{ctl} =0V, I _o =0mA (OFF MODE)
Bias Current	I _b	—	0.5	0.9	mA	I _o =0mA(V _o ≤6.0V)
		—	0.6	1.0	mA	I _o =0mA(V _o ≥7.0V)
Output Voltage	V _o	V _{o(T)} ×0.98	V _{o(T)}	V _{o(T)} ×1.02	V	I _o =200mA
Dropout Voltage ※4	ΔV _d	—	0.3	0.5	V	I _o =500mA, V _{cc} =V _o ×0.95V
Peak Output Current	I _o	1.0	—	—	A	
Ripple Rejection	R.R.	44	55	—	dB	f=120Hz, e _{in} *6=4-20dBV, I _o =100mA
Line Regulation	Reg.I	—	15	35	mV	V _{cc} =V _{ccmin} →16V, I _o =200mA
Load Regulation	Reg.L	—	35	75	mV	I _o =0mA→1A
Temperature Coefficient of Output Current ※5	T _{cvo}	—	±0.02	—	%/°C	I _o =5mA, T _j =0~125°C
Output Short Current	I _{os}	—	0.21	—	A	V _{cc} =16V(V _o =1.5V)
		—	0.25	—		V _{cc} =16V(1.8≤V _o ≤3.3)
		—	0.30	—		V _{cc} =16V(V _o ≥5.0V)
ON Mode Voltage	V _{th1}	2.0	—	—	V	ACTIVE MODE, I _o =0mA
OFF Mode Voltage	V _{th2}	—	—	0.8	V	OFF MODE, I _o =0mA
Input High Current	I _{in}	40	80	130	μA	I _o =0mA

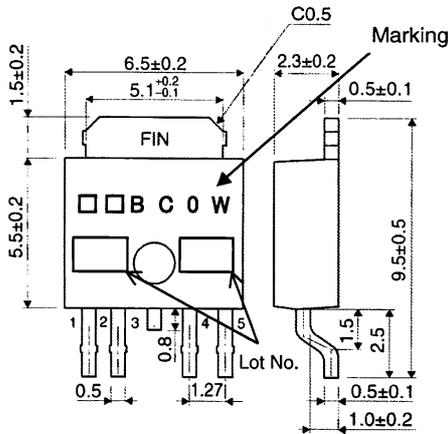
V_{o(T)}: Output Voltage

※4 V_o≥3.3V

※5 Designed Guarantee. (Outgoing inspection is not done on all products.)

※6 e_{in}=Input Voltage Ripple

○PHYSICAL DIMENSIONS, MARKING

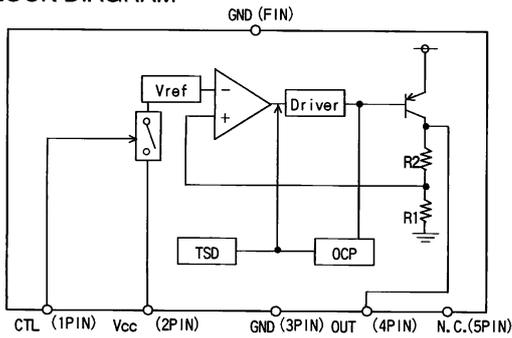


TO252-5 (UNIT : mm)

○OUTPUT VOLTAGE, MARKING

Parts Number	Marking	Output Voltage (V)	Parts Number	Marking	Output Voltage (V)
BA15BC0WFP	15BC0W	1.5	BA60BC0WFP	60BC0W	6.0
BA18BC0WFP	18BC0W	1.8	BA70BC0WFP	70BC0W	7.0
BA25BC0WFP	25BC0W	2.5	BA80BC0WFP	80BC0W	8.0
BA30BC0WFP	30BC0W	3.0	BA90BC0WFP	90BC0W	9.0
BA33BC0WFP	33BC0W	3.3	BAJ0BC0WFP	J0BC0W	10.0
BA50BC0WFP	50BC0W	5.0			

○BLOCK DIAGRAM



○PIN NO. , PIN NAME

Pin No.	Pin Name
1	CTL
2	Vcc
3	N.C.
4	OUT
5	N.C.
FIN	GND

※ Please refer to technical note concerning application circuit, and etc.

○NOTES FOR USE

- 1. Absolute maximum range**
 Absolute Maximum Ratings are those values beyond which the life of a device may be destroyed we cannot be defined the failure mode, such as short mode or open mode.
 Therefore physical security countermeasure, like fuse, is to be given when a specific mode to be beyond absolute maximum ratings is considered.
- 2. GND pin voltage**
 GND terminal should be connected the lowest voltage, under all conditions. And all terminals except GND should be under GND terminal voltage under all conditions including transient situations.
- 3. Power dissipation**
 If IC is used on condition that the power loss is over the power dissipation, the reliability will become worse by heat up, such as reduced output current capability.
 Also, be sure to use this IC within a power dissipation range allowing enough of margin.
- 4. Electrical characteristics described in these specifications may vary, depending on temperature, supply voltage, external circuits and other conditions. Therefore, be sure to check all relevant factors, including transient characteristics.**
- 5. Be sure to connect a capacitor with capacitance of at least 22μF, including temperature characteristics and variation, to prevent oscillation between the Vo and GND. Note that if the capacity of the capacitor changes due to factors such as changes in temperature or ESR, oscillation may occur, and the original characteristics of the IC may not be realized. For example, when a ceramic capacitor is employed, oscillation will be generated because the series resistance is too small. Please take countermeasures to prevent this, such as adding a series resistor. Standard electrolytic capacitors are subject to extremely large capacitance and ESR fluctuations due to temperature conditions. Particularly at low temperature, capacity is decreased, while ESR grows larger, conditions which increase the vulnerability to oscillation. Therefore, be certain to check for the presence of oscillation.**
 In respect to the proper ESR range, select a capacitor that meets the conditions in the figure below for stable operation range. Note, however, that the stable range suggested in the figure depends on the IC and the resistance load involved, and can vary with the board's wiring impedance, input impedance, and/or load impedance. Therefore, be certain to ascertain the final status of these items for actual use.
 Keep capacitor capacitance within a range of 22μF~1000μF. It is also recommended that a 0.33μF bypass capacitor be connected as close to the input pin-GND as location possible. However, in situations such as rapid fluctuation of the input voltage or the load, please check the operation in real application to determine proper capacitance.

