

Y/C/RGB/Sync/Deflection for Color TV

Description

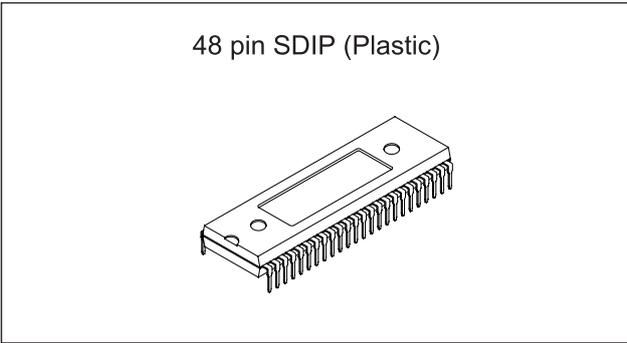
The CXA2095S is a bipolar IC which integrates the luminance signal processing, chroma signal processing, RGB signal processing, and sync and deflection signal processing functions for NTSC system color TVs onto a single chip.

The following functions have been added to the same function IC, CXA2025S.

- 1) Vertical sync pull-in speed switching function
- 2) YUV SW Y signal switching function
- 3) fsc output pin

Features

- I²C bus compatible
- Sync signal processing uses a countdown system with non-adjusting H/V oscillator frequencies
- Built-in deflection compensation circuit capable of supporting various wide modes
- Non-adjusting Y/C block filter
- Built-in AKB
- Video signal I/Os: Y/C separation input, Y/color difference input, analog RGB input and RGB output
- YUV SW Y signal switching function allows picture quality adjustment for the Y signal in the same manner as for the normal Y signal even when Y/color difference input is selected



Absolute Maximum Ratings

(Ta = 25°C, SGND, JGND = 0V)

- Supply voltage SVcc, JVcc -0.3 to +12 V
- Operating temperature
 - Topr -20 to +75 °C
- Storage temperature
 - Tstg -65 to +150 °C
- Allowable power dissipation
 - Pd 1.5 W
- Voltages at each pin -0.3 to SVcc, JVcc + 0.3 V

Operating Conditions

Supply voltage	SVcc	9.0 ± 0.5	V
	JVcc	9.0 ± 0.5	V

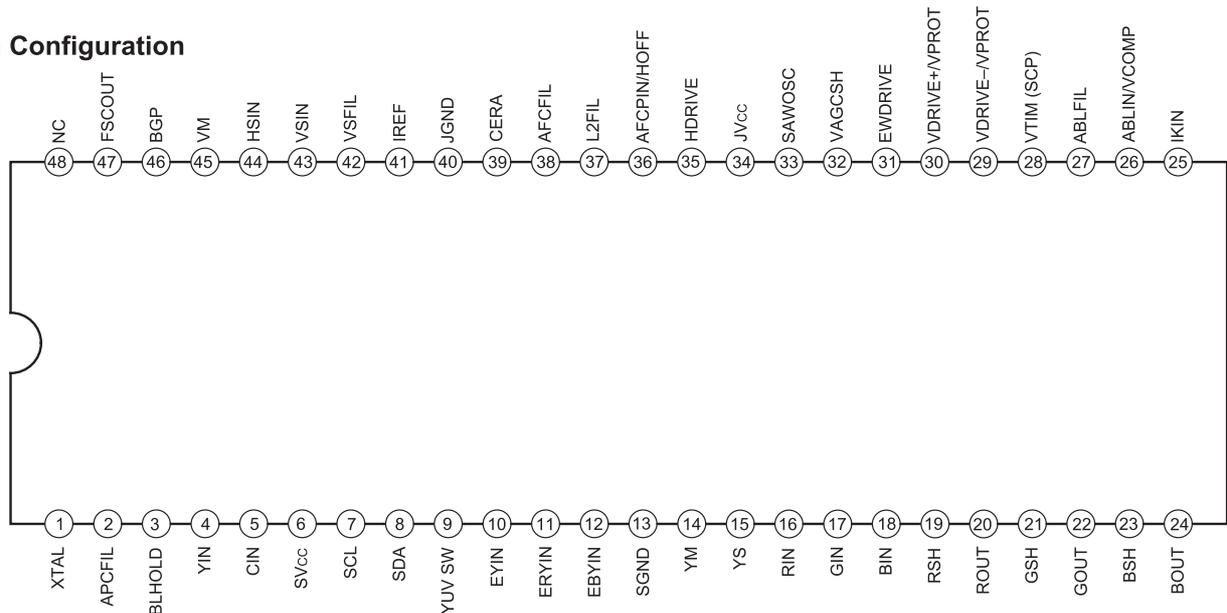
Applications

Color TVs (4:3, 16:9)

Structure

Bipolar silicon monolithic IC

Pin Configuration



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Block Diagram

