



Features

1. CPU configuration
2. 1K 13 bits on chip ROM
3. 32 8 bits on-chip registers (SRAM, general purpose)
4. 5 level stacks for subroutine nesting
5. Less than 1.5 mA at 5V/4MHz
6. Typically 15 μ A, at 3V/32kHz

HS153SP-J is likely the mask ROM version of the EM78P153SP.