

# TA7739P/F

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## 3V DUAL PREAMPLIFIER.

The TA7739P(DIP-16) and TA7739F(MFP-16-SO) are dual preamplifier designed for a 3V operation automatic reverse type headphone stereo application.

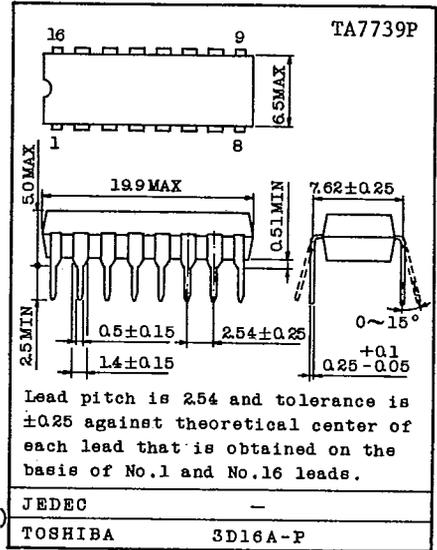
- Built-in Forward/Reverse Control Switch
- Built-in Metal/Normal Control Switch
- No Input Coupling Capacitor
- Low Noise :  $V_{NI}=0.9\mu V_{rms}$ (Typ.)  
( $R_g=2.2k\Omega$ ,  $NAB(G_v=40dB$ ,  $f=1kHz)$ )
- Low Supply Current  
:  $I_{CCQ}=1.8mA$ (Typ.) ( $V_{CC}=3V$ ,  $V_{IN}=0V$ )
- High Output Voltage  
:  $V_{OM}=0.9V_{rms}$ (Typ.) ( $THD=0.5%$ ,  $V_{CC}=3V$ )
- Wide Operating Supply Voltage :  $V_{CC}=1.6V\sim 5V$  ( $T_a=25^\circ C$ )

### MAXIMUM RATINGS ( $T_a=25^\circ C$ )

CHARACTERISTIC		SYMBOL	RATING	UNIT
Supply Voltage		$V_{CC}$	6	V
Power Dissipation (Note)	TA7739P	$P_{D1}$	750	mW
	TA7739F	$P_{D2}$	350	
Operating Temperature		$T_{opr}$	-25~75	$^\circ C$
Storage Temperature		$T_{stg}$	-55~150	$^\circ C$

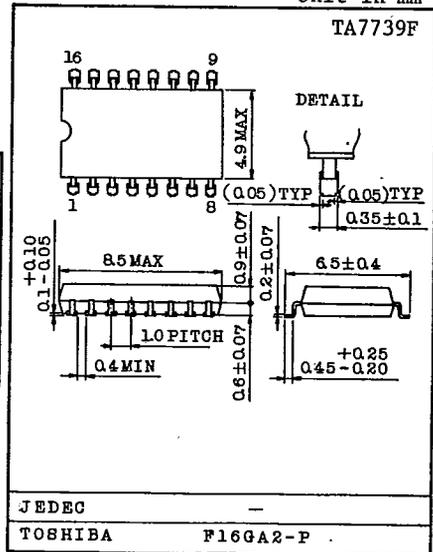
Note: Derated above  $T_a=25^\circ C$  in the proportion of  $6mW/^\circ C$  for TA7739P and of  $2.8mW/^\circ C$  for TA7739F.

Unit in mm



Weight : 1.00g

Unit in mm



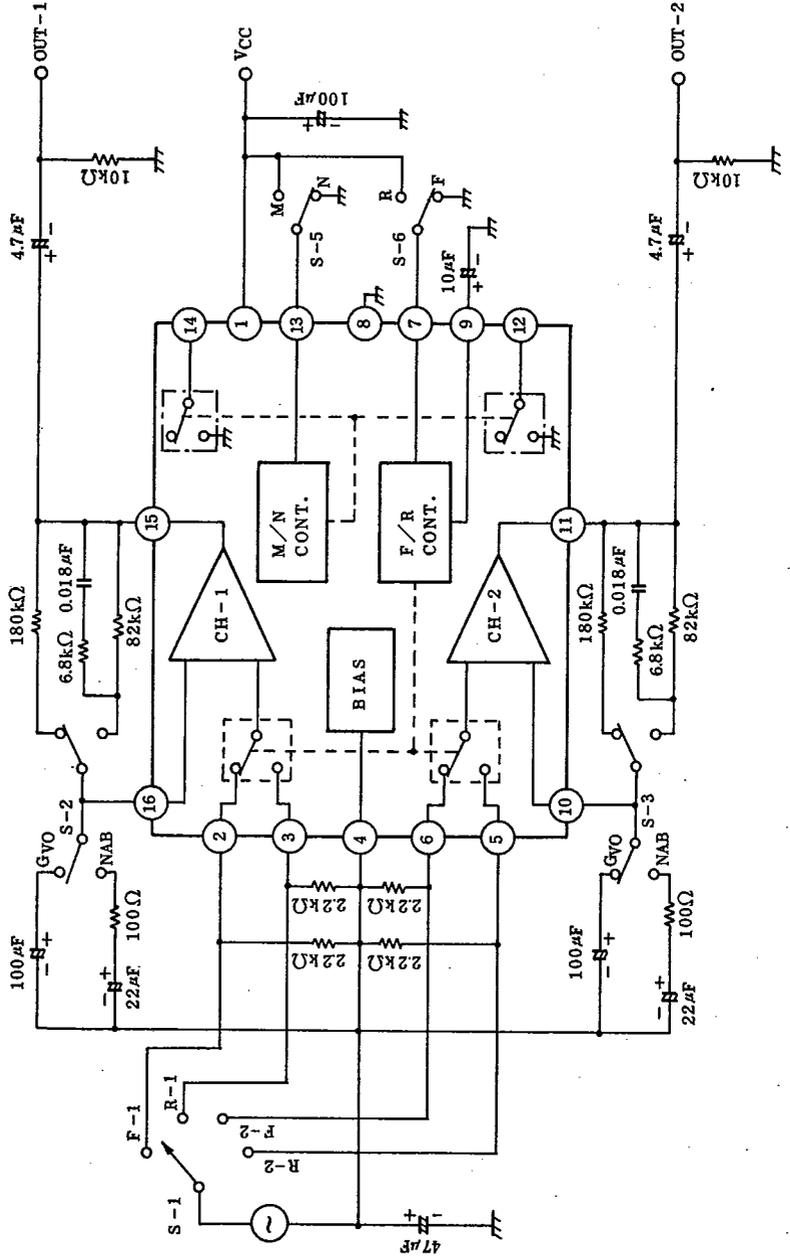
TA7739F : 0.14g

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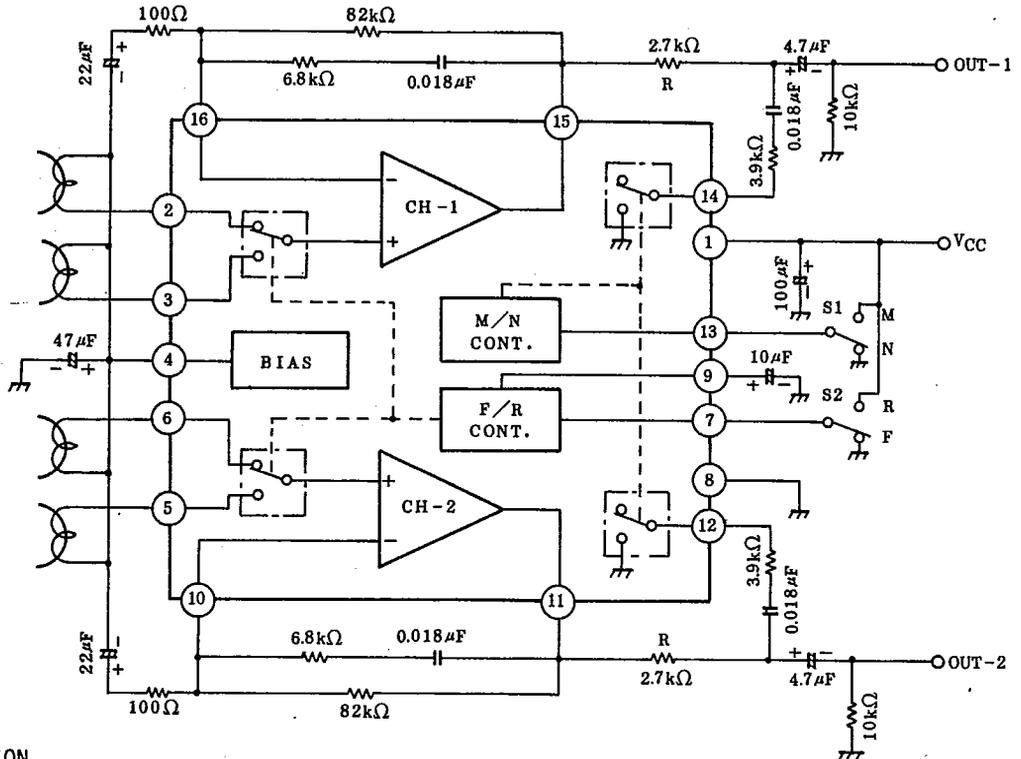
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BLOCK DIAGRAM/TEST CIRCUIT



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## APPLICATION CIRCUIT



### CAUTION

- Capacitive load should not be directly connected to output terminal to avoid instable operation.
- When closed loop voltage gain ( $G_v$ ) is set below 40dB, a series resistor more than 1kΩ connected to the output terminal is needed. Minimum closed loop voltage gain should be more than 21dB.
- About threshold level of S1 and S2, "H" level is above 0.9V, "L" level is below 0.3V.

