

## 2 Pin configuration

Figure 2: Pin connection (top view, bottom view for TO-92)

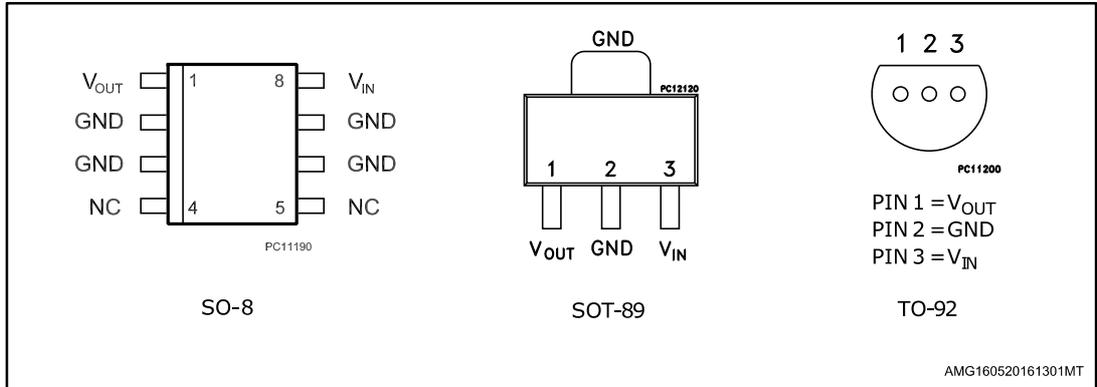


Figure 3: Test circuits

