
M65582AMF-XXXFP

NTSC TV Signal Processor with MCU

REJ03F0093-0100Z

Rev.1.0

Sep.19.2003

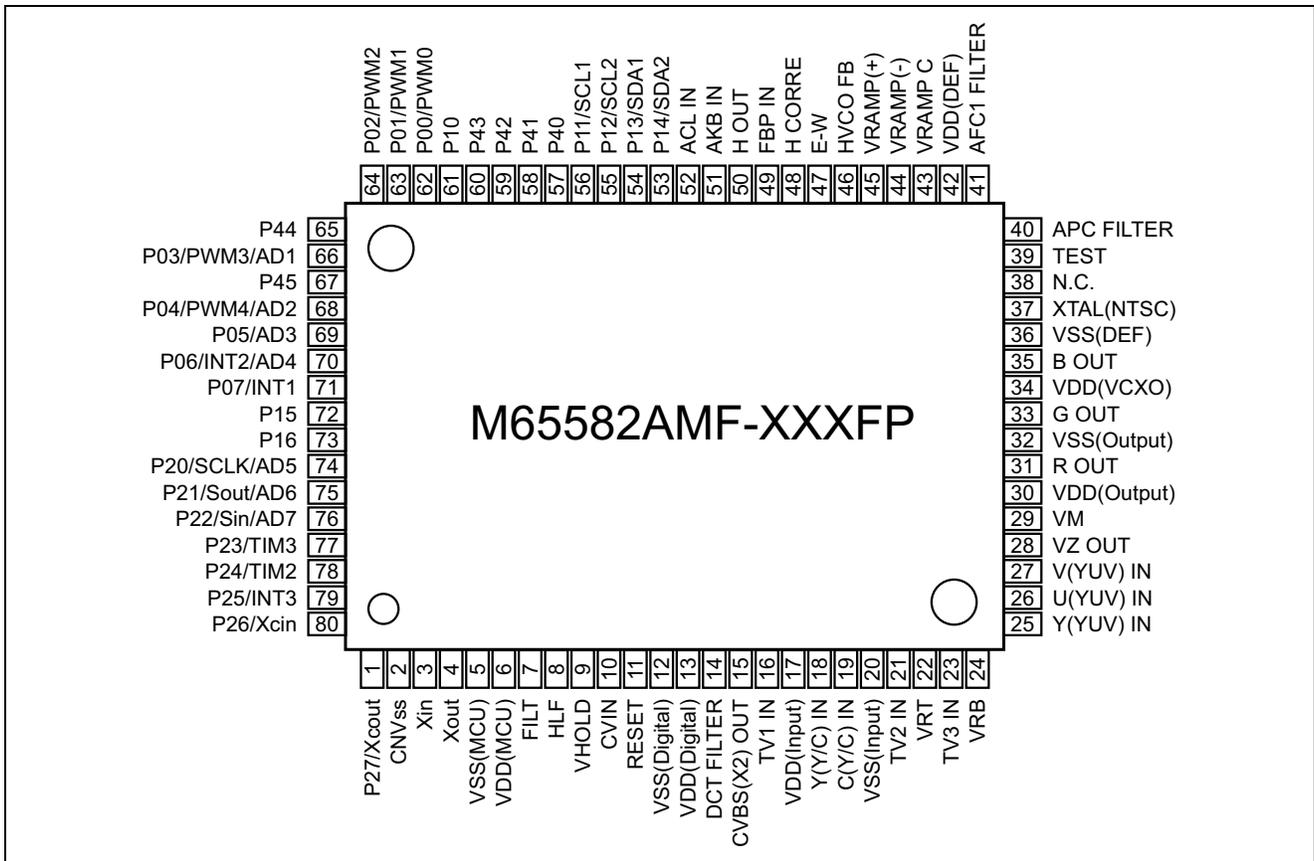
Features

- 1package solution with TV baseband signals (Video and Chroma) processor, deflection and 8bit MCU
- High quality picture by 2 Dimension Adaptive Y/C Separation of 3 Line type
- Built-in VM (Velocity Modulation) circuit emphasizing the picture outline by the changing of the Scanning Speed
- Built-in the correction circuits of the picture distortion which is EAST-WEST function etc. for Flat TV
- Available to use the software for best saled MCU M37272
- Available to input External Video signal, S Video signal and Component Video signal
- High performance OSD function with CCD and Half Tone Display
- Analog Video Switch with 5 Video Inputs Composite Video : 3ch, S Video : 1ch, Component Video : 1ch
- Built-in a high performance Blackstrech
- Built-in YNR
- Built-in 8bit MCU core M37272
ROM : 60Kbyte, RAM : 2048byte

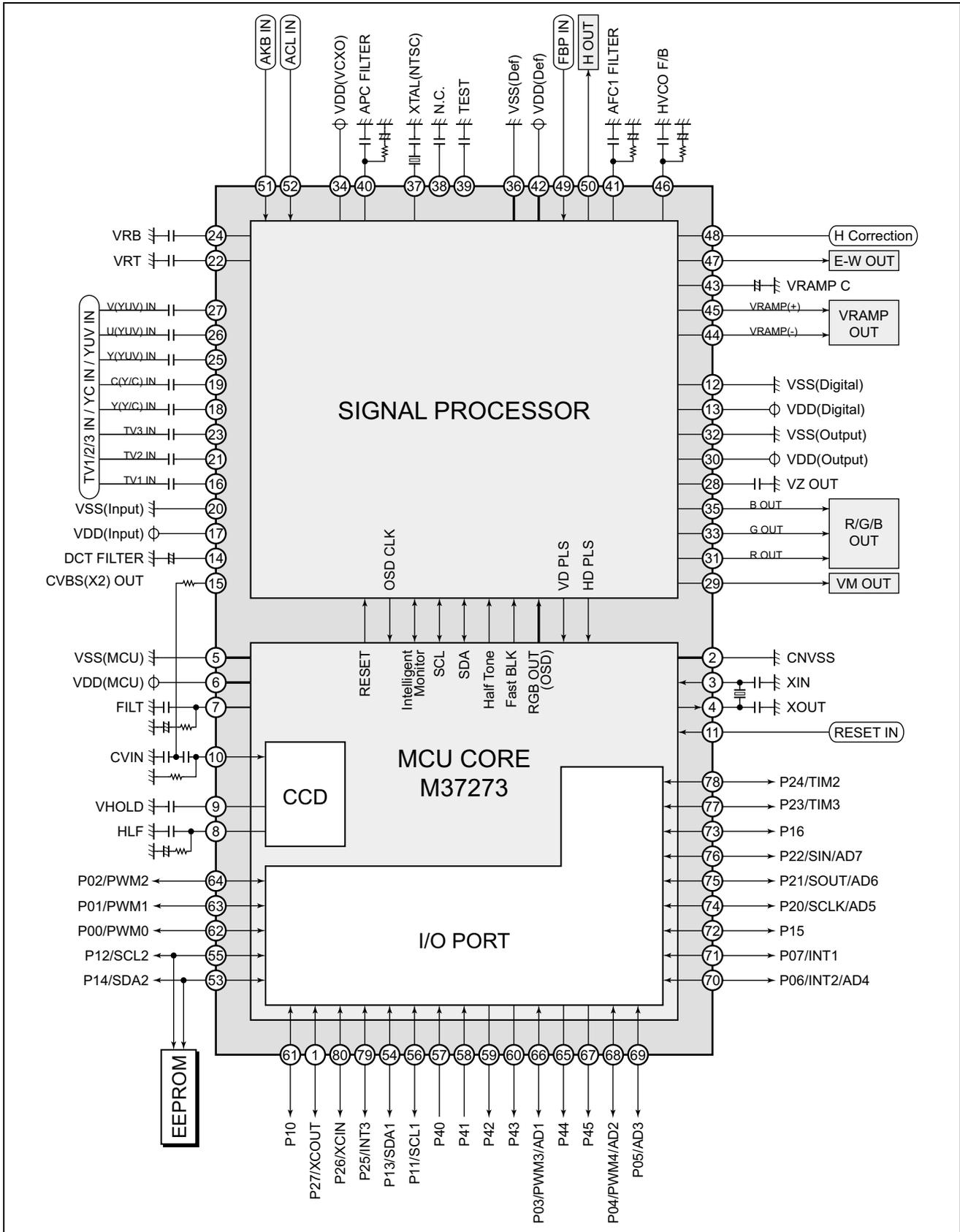
Applications

NTSC color television receivers

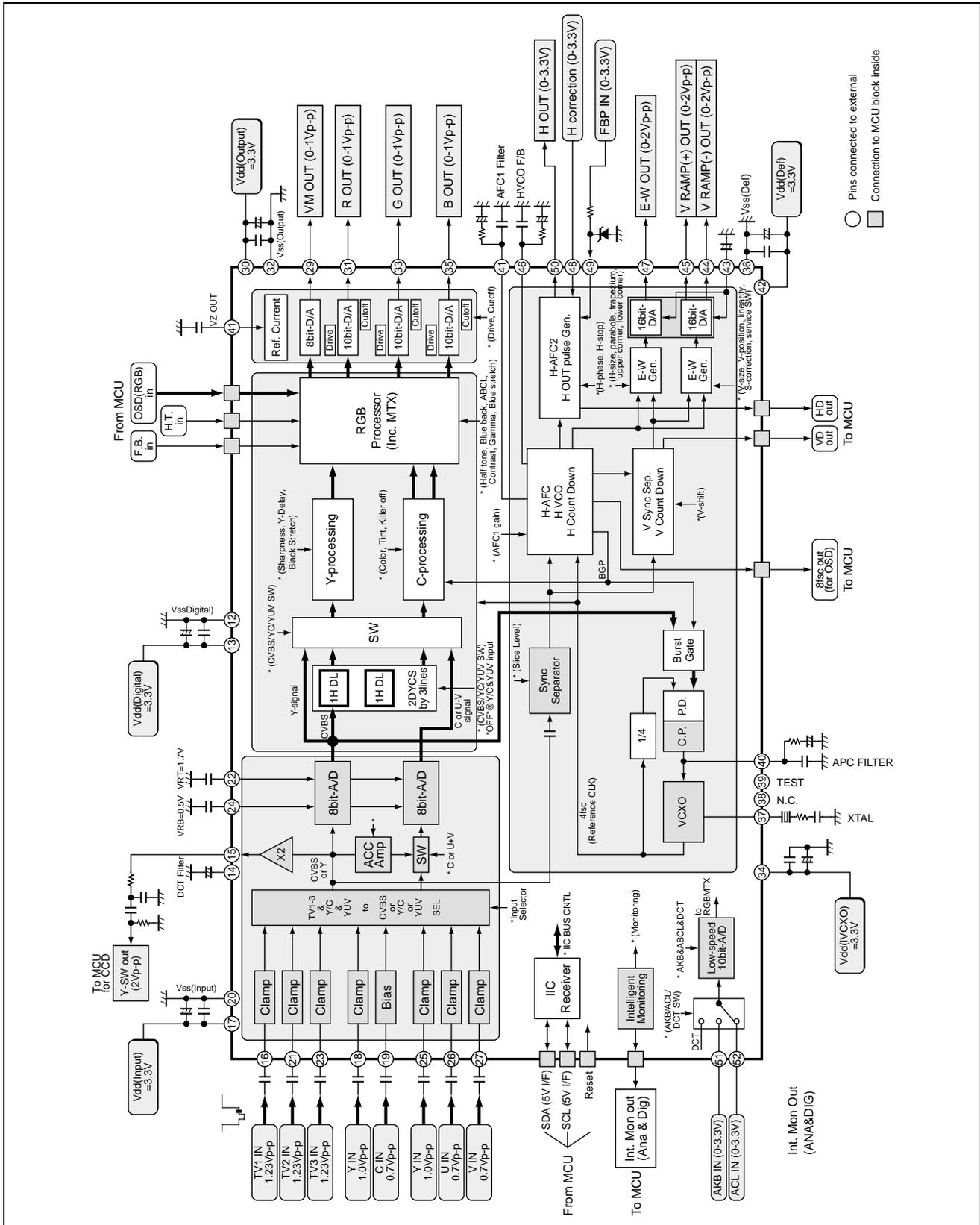
Pin Configuration



Block Diagram (Whole)



Block Diagram (ASIC)



Absolute maximum ratings

Parameter	Symbol	Ratings	Unit	Conditions
Supply voltage (MCU : 5V)	VDD (MCU)	-0.3 to 6.0	V	All voltage are based on Vss. Output transistors are cut off.
Supply voltage (ASIC : 3.3V)	VDD (ASIC)	-0.3 to 4.0	V	
Input Voltage (MCU)	VI (MCU)	-0.3 to Vcc+0.3	V	
Output Voltage (MCU)	VO (MCU)	-0.3 to Vcc+0.3	V	
Circuit current (MCU)	IOH (MCU)	0 to 1 (See note 1)	mA	
Circuit current (P00-P07, P10, P15, P16, P20-P27, P40-P45)	IOL1 (MCU)	0 to 2 (See note 2)	mA	
Circuit current (P11-P14)	IOL2 (MCU)	0 to 6 (See note 2)	mA	
Circuit current (P24-P27)	IOL3 (MCU)	10 (See note 3)	mA	
Digital input voltage	VID (ASIC)	-0.3 to Vcc+0.3	V	
Analog output current	IOOUT (ASIC)	-30	mA	
Power dissipation	Pd	2000	mW	
Thermal derating	Kt	20.0	mW/°C	
Operating temperature	Topr	-20 to 70	°C	
Storage temperature	Tstg	-40 to 125	°C	

Recommended Conditions

(Ta=25 to 70°C, Unless otherwise noted)

Parameter	Symbol	Limits			Unit
		Min.	Typ.	Max.	
Supply voltage (MCU) (See note 4)	VDD (MCU)	4.75	5.0	5.25	V
Supply voltage (Digital)	VDD (Digital)	3.13	3.3	3.47	V
Supply voltage (Input)	VDD (Input)	3.13	3.3	3.47	V
Supply voltage (Output)	VDD (Output)	3.13	3.3	3.47	V
Supply voltage (VCXO)	VDD (VCXO)	3.13	3.3	3.47	V
Supply voltage (DEF)	VDD (DEF)	3.13	3.3	3.47	V
Supply voltage (MCU)	VSS (MCU)	0	0	0	V
High loutput voltage P00-P07, P10-P16, P20-P27, P40-P45, RESET, X IN	VIH1 (MCU)	0.8 VDD		VDD	V
High loutput voltage SCL1, SCL2, SDA1, SDA2 (When using I2C -Bus)	VIH2 (MCU)	0.7 VDD		VDD	V
High loutput voltage FBP IN	VIH3 (ASIC)	0.8 VDD		VDD	V
Low loutput voltage P00-P07, P10-P16, P20-P27 P40-P45	VIL1 (MCU)	0		0.4 VDD	V
Low loutput voltage SCL1, SCL2, SDA1, SDA2 (When using I2C-Bus)	VIL2 (MCU)	0		0.3 VDD	V
Low loutput voltage (See note 6) RESETB, X IN, TIM2, TIM3, INT1, INT2, INT3, S IN, S CLK	VIL3 (MCU)	0		0.2 VDD	V
Low loutput voltage FBP IN	VIL4 (ASIC)	0		0.2 VDD	V
High average output current (See note 1) P10-P16, P20-P27, P40-P45	IOH (MCU)			1	mA