MJE340G

Plastic Medium-Power NPN Silicon Transistor

This device is useful for high−voltage general purpose applications.

Features

- Suitable for Transformerless, Line−Operated Equipment
- Thermopad Construction Provides High Power Dissipation Rating for High Reliability
- These Devices are Pb−Free and are RoHS Compliant*
- Complementary to MJE350

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector−Emitter Voltage	V_{CEO}	300	Vdc
Emitter−Base Voltage	V_{EB}	3.0	Vdc
Collector Current − Continuous	I_C	500	mAdc
Total Power Dissipation @ T_C = 25°C Derate above 25°C	P_D	 20 0.16	 W mW/°C
Operating and Storage Junction Temperature Range	T_J, T_{stg}	–65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction−to−Case	θ_{JC}	6.25	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector−Emitter Sustaining Voltage (I_C = 1.0 mAdc, I_B = 0)	$V_{CEO(sus)}$	300	–	Vdc
Collector Cutoff Current (V_{CB} = 300 Vdc, I_E = 0)	I_{CBO}	–	100	μAdc
Emitter Cutoff Current (V_{EB} = 3.0 Vdc, I_C = 0)	I_{EBO}	–	100	μAdc
ON CHARACTERISTICS				
DC Current Gain (I_C = 50 mAdc, V_{CE} = 10 Vdc)	h_{FE}	30	240	–

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

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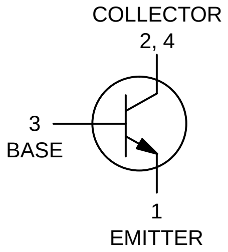
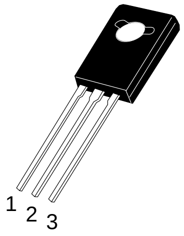
0.5 AMPERE
POWER TRANSISTOR
NPN SILICON
300 VOLTS, 20 WATTS

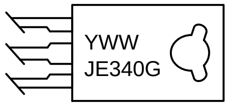
SCHEMATIC





TO−225
CASE 77−09
STYLE 1

MARKING DIAGRAM



Y	= Year
WW	= Work Week
JE340	= Device Code
G	= Pb−Free Package

ORDERING INFORMATION

Device	Package	Shipping
MJE340G	TO−225 (Pb−Free)	500 Units/Box

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*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

December, 2013 − Rev. 13