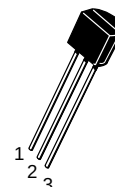
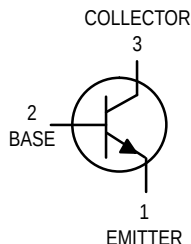


One Watt Amplifier Transistors

NPN Silicon

MPSW05
MPSW06*

*Motorola Preferred Device



CASE 29-05, STYLE 1
TO-92 (TO-226AE)

MAXIMUM RATINGS

Rating	Symbol	MPSW05	MPSW06	Unit
Collector–Emitter Voltage	V_{CEO}	60	80	Vdc
Collector–Base Voltage	V_{CBO}	60	80	Vdc
Emitter–Base Voltage	V_{EBO}	4.0		Vdc
Collector Current — Continuous	I_C	500		mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	1.0 8.0		Watt mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	2.5 20		Watts mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	–55 to +150		$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	125	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case	$R_{\theta JC}$	50	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage ⁽¹⁾ ($I_C = 1.0 \text{ mAdc}$, $I_B = 0$)	MPSW05 MPSW06	$V_{(BR)CEO}$	60 80	— —	Vdc
Emitter–Base Breakdown Voltage ($I_E = 100 \mu\text{Adc}$, $I_C = 0$)		$V_{(BR)EBO}$	4.0	—	Vdc
Collector Cutoff Current ($V_{CE} = 40 \text{ Vdc}$, $I_B = 0$) ($V_{CE} = 60 \text{ Vdc}$, $I_B = 0$)	MPSW05 MPSW06	I_{CES}	— —	0.5 0.5	μAdc
Collector Cutoff Current ($V_{CB} = 40 \text{ Vdc}$, $I_E = 0$) ($V_{CB} = 60 \text{ Vdc}$, $I_E = 0$)	MPSW05 MPSW06	I_{CBO}	— —	0.1 0.1	μAdc
Emitter Cutoff Current ($V_{EB} = 3.0 \text{ Vdc}$, $I_C = 0$)		I_{EBO}	—	0.1	μAdc

1. Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.

Preferred devices are Motorola recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit
ON CHARACTERISTICS(1)				
DC Current Gain (I _C = 50 mAdc, V _{CE} = 1.0 Vdc) (I _C = 250 mAdc, V _{CE} = 1.0 Vdc)	h _{FE}	80 60	— —	—
Collector–Emitter Saturation Voltage (I _C = 250 mAdc, I _B = 10 mAdc)	V _{CE(sat)}	—	0.4	Vdc
Base–Emitter Saturation Voltage (I _C = 250 mAdc, V _{CE} = 5.0 Vdc)	V _{BE(sat)}	—	1.2	Vdc
SMALL–SIGNAL CHARACTERISTICS				
Current–Gain — Bandwidth Product (I _C = 200 mAdc, V _{CE} = 5.0 Vdc, f = 20 MHz)	f _T	50	—	MHz
Output Capacitance (V _{CB} = 10 V, f = 1.0 MHz)	C _{obo}	—	12	pF

1. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

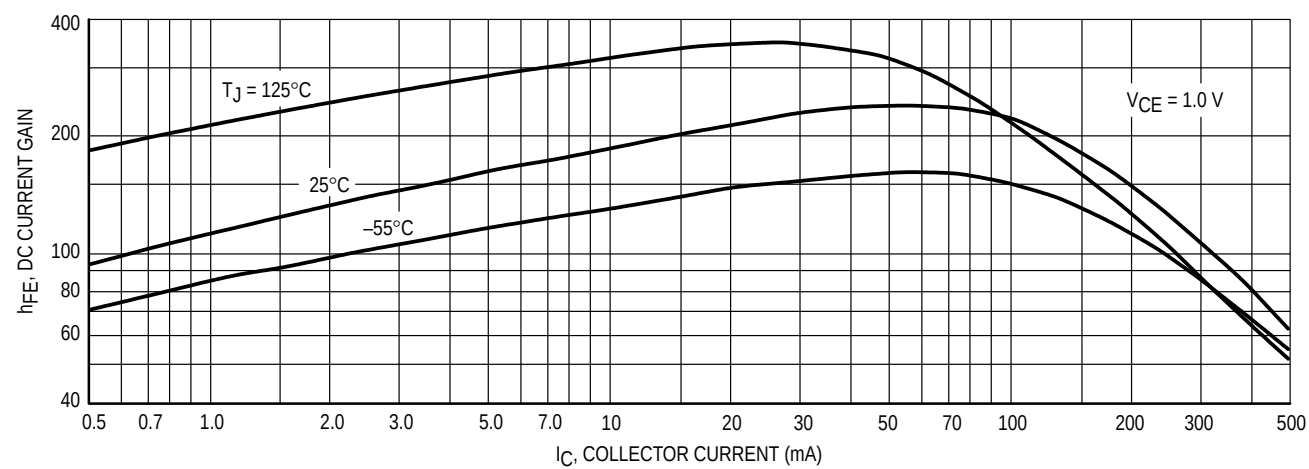


Figure 1. DC Current Gain

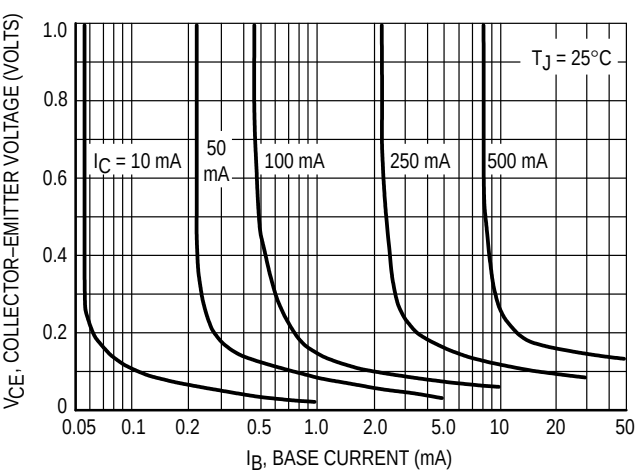


Figure 2. Collector Saturation Region

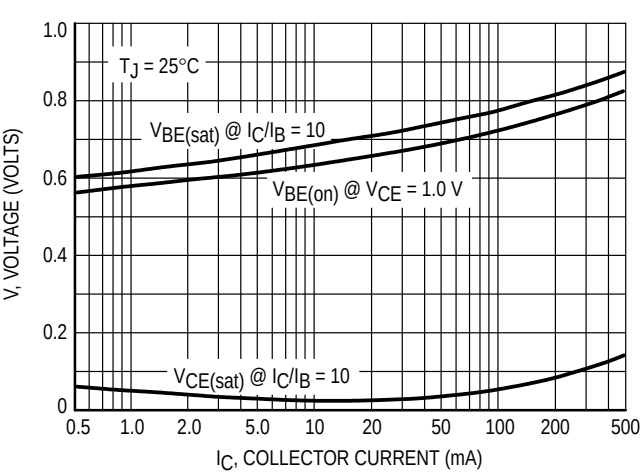


Figure 3. "On" Voltages

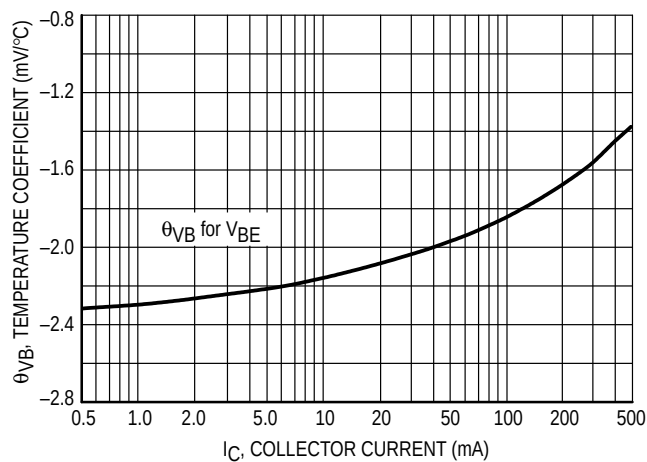


Figure 4. Base-Emitter Temperature Coefficient

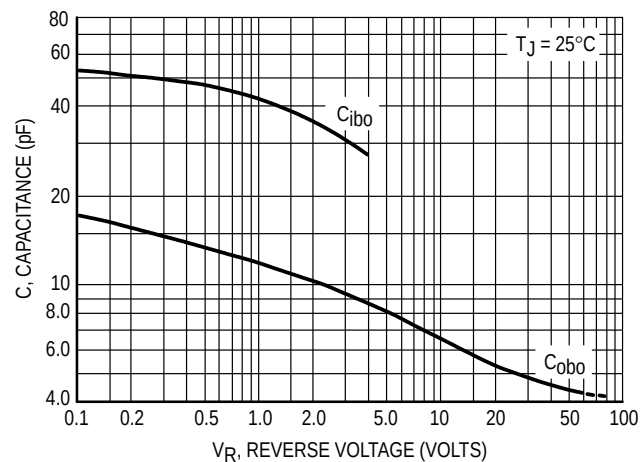


Figure 5. Capacitance

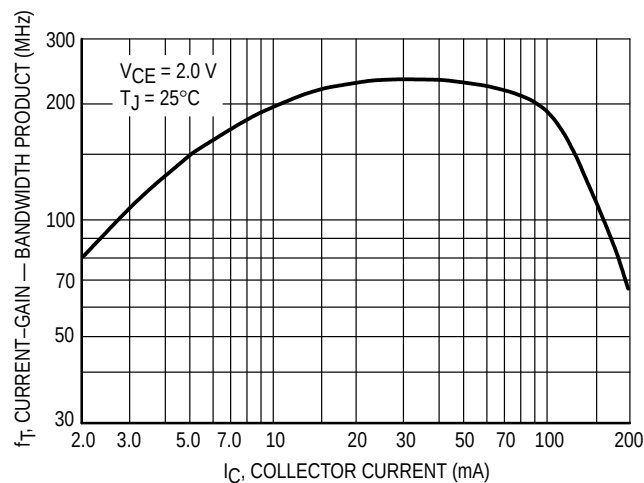


Figure 6. Current-Gain — Bandwidth Product

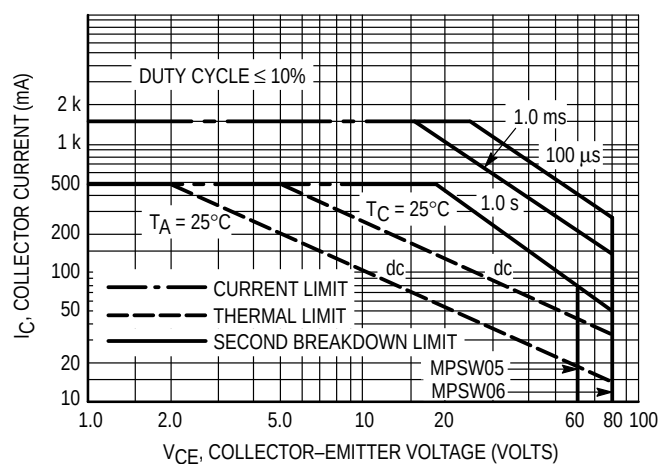
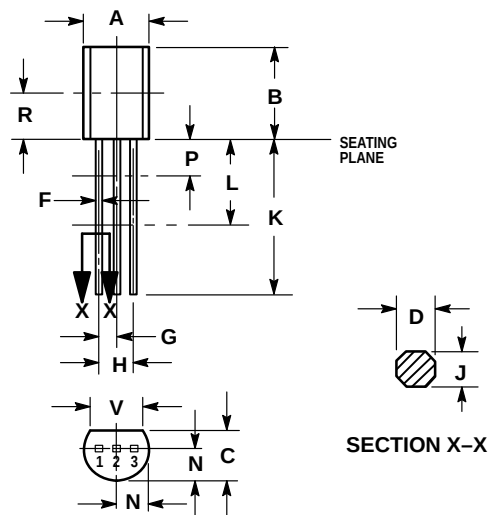


Figure 7. Active Region — Safe Operating Area

PACKAGE DIMENSIONS



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSIONS D AND J APPLY BETWEEN L AND K MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.44	5.21
B	0.290	0.310	7.37	7.87
C	0.125	0.165	3.18	4.19
D	0.018	0.022	0.46	0.56
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.018	0.024	0.46	0.61
K	0.500	—	12.70	—
L	0.250	—	6.35	—
N	0.080	0.105	2.04	2.66
P	—	0.100	—	2.54
R	0.135	—	3.43	—
V	0.135	—	3.43	—

STYLE 1:

1. PIN 1. EMITTER
2. BASE
3. COLLECTOR

**CASE 029-05
(TO-226AE)
ISSUE AD**

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