

N-Channel Super Junction Power MOSFET III

General Description

The series of devices use advanced trench gate super junction technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

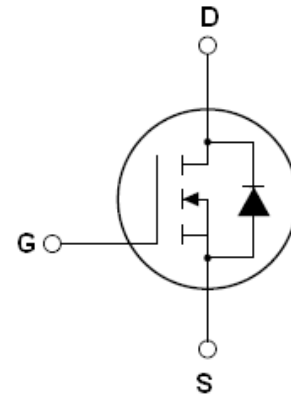
Features

- New technology for high voltage device
- Low on-resistance and low conduction losses
- Small package
- Ultra Low Gate Charge cause lower driving requirements
- 100% Avalanche Tested
- ROHS compliant

Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)

V_{DS}	700	V
$R_{DS(ON)TYP.}$	1100	m Ω
I_D	4	A



Schematic diagram

Package Marking And Ordering Information

Device	Device Package	Marking
NCE70T1K2I	TO-251	NCE70T1K2I
NCE70T1K2K	TO-252	NCE70T1K2K



TO-252



TO-251

Table 1. Absolute Maximum Ratings ($T_c=25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Drain-Source Voltage ($V_{GS}=0V$)	V_{DS}	700	V
Gate-Source Voltage ($V_{DS}=0V$), AC ($f>1\text{ Hz}$)	V_{GS}	± 30	V
Continuous Drain Current at $T_c=25^\circ\text{C}$	$I_{D(DC)}$	4	A
Continuous Drain Current at $T_c=100^\circ\text{C}$	$I_{D(DC)}$	2.5	A
Pulsed drain current (Note 1)	$I_{DM(pluse)}$	16	A
Maximum Power Dissipation ($T_c=25^\circ\text{C}$)	P_D	41	W
Derate above 25°C		0.328	W/ $^\circ\text{C}$
Single pulse avalanche energy (Note2)	E_{AS}	27	mJ
Avalanche current (Note 1)	I_{AR}	0.7	A
Repetitive Avalanche energy, t_{AR} limited by T_{jmax} (Note 1)	E_{AR}	0.1	mJ